

What is claimed is:

1. An organic electroluminescent display comprising:
a substrate;
at least a thin-film transistor, which is formed on the substrate;
5 at least an insulation layer, which cover the thin-film transistor;
first electrodes, which are formed in a predetermined pattern on a top surface
of the insulation layer and to which a voltage is selectively applied through the
thin-film transistor;
bus electrodes, which are insulated from the first electrodes;
10 a planarization layer, which is an insulation layer and has openings exposing
the first electrodes and the bus electrodes;
organic layers, which are formed on a top surface of the first electrodes; and
second electrodes, which are formed on a top surface of the organic layer and
a top surface of the planarization layer and are electrically connected to the bus
15 electrodes.

2. The organic electroluminescent display of claim 1, wherein the second
electrodes are made of a transparent material.

20 3. The organic electroluminescent display of claim 1, wherein the first
electrodes and the bus electrodes are made of the same material.

4. The organic electroluminescent display of claim 3, wherein the first
electrodes and the bus electrodes are made of metal.

25 5. The organic electroluminescent display of claim 1, wherein the bus
electrodes are formed on a top surface of the insulation layer.

30 6. The organic electroluminescent display of claim 1, wherein light emitted
from the organic layers is discharged in a direction of the second electrodes.

7. A method of manufacturing an organic electroluminescent display, the
method comprising:
forming at least a thin-film transistor on a top surface of a substrate;

forming at least an insulation layer on a top surface of the thin-film transistor;
forming first electrodes, to which a potential is selectively applied through the
thin-film transistor, and bus electrodes, which are electrically insulated from the first
electrodes on a top surface of the insulation layer;

5 forming a planarization layer on a top surface of the insulation layer to have
openings at positions corresponding to the first electrodes and the bus electrodes;

forming organic layers on a top surface of the first electrodes; and

forming second electrodes on a top surface of the planarization layer and a
top surface of selected organic layers and are electrically connected to the bus

10 electrodes.